

Figure 2. Control panel layout

Common Entry Channel (R. I.)

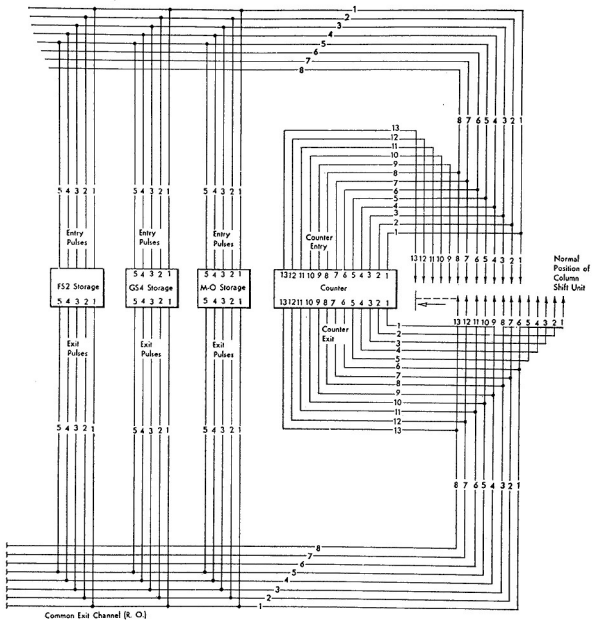


Figure 3. Interconnections between storage units

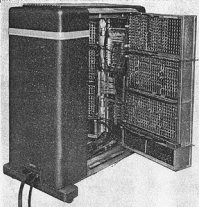


Figure 4 (above). Arithmetic unit, left gate open

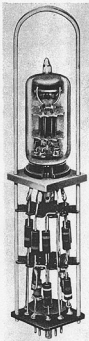
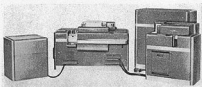


Figure 5 (right). Pluggable circuit unit

Figure 6 (below). The CPC



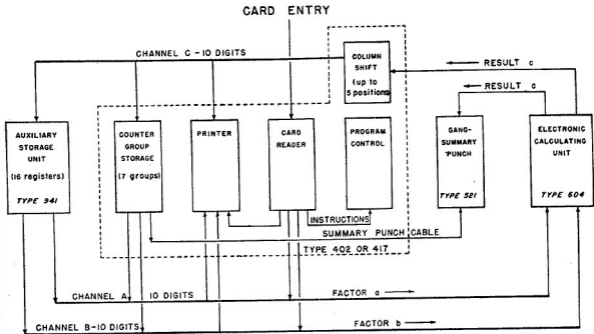


Figure 7. Connections between units by function

